* TF = 1/12 x 11.059 MHz = 921583 Hz.

**Timer Clock Period**

The time delay of one machine cycle is given below. We use this to generate the delay.

* TP = 1/921583 = 1.085 µ sec.
* Firstly, we divide the desired time delay value (10 ms) by the timer clock period.
  + N =  1/1.085us
  + N = 10ms/1.0859usec
  + N = 9216
* Now we will subtract the value of N from the maximum number of counts possible for the 16-bit timer, i.e., 216 = 65536.
  + M = 65536 – N
  + M = 65536 – 9216
  + M = 56320
* Lastly, we convert this value to hexadecimal and write it in the TH and TL registers.
  + MH = DC00
  + TH = DC
  + TL = 00